

**AMENDMENTS TO THE SPECIFICATION**

**Please replace the following two paragraphs beginning with the fourth paragraph on page 14 and continuing to the second paragraph on page 15 with the following amended paragraphs:**

Next will be described a second embodiment of the present invention. Fig. 3 shows a schematic configuration of serial digital signal transmission apparatus, which is another embodiment of the invention. In Fig. 3, the same constituent elements as in Fig. 1(a) and Fig. 1(b) are represented by respectively the same reference signs. In Fig. 3, an RTS generator circuit 21 has a circuit configuration other than that of the SP converter 1 shown in Fig. 1(a), and an RTS receiver circuit 23 has a circuit configuration other than that of the PS converter 14 shown in Fig. 1(b). In the second embodiment of the invention, the count of "N" of the N counter 2 in the RTS generator circuit 21 and the value of multiplier "N" of the PLL circuit 13 in the RTS ~~generator~~receiver circuit 23 are both "8", and the ATM cell structure is provided by an AAL1 circuit (ATM cell processing unit) 22 whose ATM adaptation layer (AAL) is of type 1 as shown in Fig. 4.

Thus, the AAL1 circuit 22 of Fig. 3 generates and supplies ATM cells of the structure shown in Fig. 4 on the basis of RTS entered from the RTS generator circuit 21 and clocks and data entered from the SP converter 1, receives ATM cells of the structure shown in Fig. 4, entered from outside and, separating the data and RTS from the ATM cells, supplies the data to the PS converter 14 and RTS to the RTS ~~generator~~receiver circuit 23 to regenerate clocks.